

Notice of References Cited	Application/Control No. 10/050,274	Applicant(s)/Patent Under Reexamination YANG, YOON SEOK	
	Examiner John Elmore	Art Unit 2134	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,182,216	01-2001	Luyster, Frank C.	713/168
	B	US-			
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mroczkowski, P., "Implementation of the block cipher Rijndael using Altera FPGA," May 2000.
	V	Daemen, J., "AES Proposal: Rijndael," March 1999, pp. 1-28.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.